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**Title of
Invention**

ON CHIP RESISTOR CALIBRATION STRUCTURE AND METHOD

Submission Type : Utility Patent Filing

Application Number:

10/605567



EFS ID:

49057

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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IGEN77	Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either <us-access-code> not found in fee-xml OR actual access code not provided in the function call.

First Named Applicant: Terence Hook

Attorney Docket Number: BUR920030074US1

Timestamp: 2003-10-09 11:06:27 EDT

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-assignment	BUR920030074US1-usasn.xml	2146	2003-10-09
us-assignment	us-assignment.xml	8983	2003-10-09
us-assignment	us-assignment.dtd	10898	2003-10-09
us-assignment	BUR920030074assign1.tif	67256	2003-10-09
us-assignment	BUR920030074assign2.tif	10688	2003-10-09
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us-fee-sheet	BUR920030074US1-usfees.xml	1997	2003-10-09
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us-declaration	BUR920030074dec2.tif	10416	2003-10-09
application-body	BUR920030074spec-trans.xml	29627	2003-10-09
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application-body	application-body.dtd	49498	2003-10-09
application-body	wipo.ent	4956	2003-10-09
application-body	mathml2.dtd	54588	2003-10-09
application-body	mathml2-qname-1.mod	13225	2003-10-09
application-body	isoamsa.ent	5191	2003-10-09
application-body	isoamsb.ent	3988	2003-10-09
application-body	isoamsc.ent	1460	2003-10-09

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application-body	BUR920030074US1FI(Gdrawing3.tif	12388	2003-10-09
application-body	BUR920030074US1FI(Gdrawing4.tif	13836	2003-10-09
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